



CINT2000 Result

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Hewlett-Packard Company
ProLiant ML370 G4 (3.4GHz, Intel Xeon)

SPECint_rate2000 = 17.3
SPECint_rate_base2000 = 16.9

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jul-2004 Hardware Avail: Aug-2004 Software Avail: Aug-2004

| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip | 1 | 125 | 13.0 | 1 | 125 | 13.0 |
| 175.vpr | 1 | 147 | 11.0 | 1 | 143 | 11.4 |
| 176.gcc | 1 | 61.2 | 20.9 | 1 | 61.2 | 20.9 |
| 181.mcf | 1 | 150 | 13.9 | 1 | 150 | 13.9 |
| 186.crafty | 1 | 79.2 | 14.6 | 1 | 79.3 | 14.6 |
| 197.parser | 1 | 139 | 15.0 | 1 | 139 | 15.0 |
| 252.eon | 1 | 77.9 | 19.4 | 1 | 65.2 | 23.1 |
| 253.perlbnk | 1 | 102 | 20.4 | 1 | 99.2 | 21.0 |
| 254.gap | 1 | 62.6 | 20.4 | 1 | 61.6 | 20.7 |
| 255.vortex | 1 | 73.0 | 30.2 | 1 | 73.0 | 30.2 |
| 256.bzip2 | 1 | 141 | 12.4 | 1 | 137 | 12.7 |
| 300.twolf | 1 | 179 | 19.5 | 1 | 179 | 19.5 |

Hardware

CPU: Intel Xeon (3.4GHz, 1MB L2, 800MHz bus)
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable: 1,2
Parallel: --
Primary Cache: 16K micro-ops I + 16KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 8x512MB DDR2-400
Disk Subsystem: 1x18.2GB 15K Ultra320 SCSI
Other Hardware:

Software

Operating System: Windows Server 2003 Standard (Build 3790)
Compiler: Intel C++ Compiler 8.0 Build 20031211Z
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)
Base tuning for C programs: -QxP -fast +FDO* shlw32M.lib
Base tuning for C++ programs: -QxP -fast -GX -GR
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -QxP -fast -Oa +FDO
175.vpr: -QxP -fast +FDO
176.gcc: basepeak = 1
181.mcf: -QxP -fast +FDO shlw32M.lib
186.crafty: -QxP -fast -Oa +FDO shlw32M.lib
197.parser: -QxP -fast +FDO
252.eon: -QxP -O2 -Qipo +FDO*
253.perlbnk: -fast -Oa +FDO shlw32M.lib
254.gap: -QxP -fast +FDO
255.vortex: -QxP -fast +FDO shlw32M.lib
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll11 +FDO
300.twolf: -QxP -fast -O3 +FDO shlw32M.lib ONESTEP=yes
BIOS Configuration Notes
HT Technology disabled