SPEC CINT95 Results

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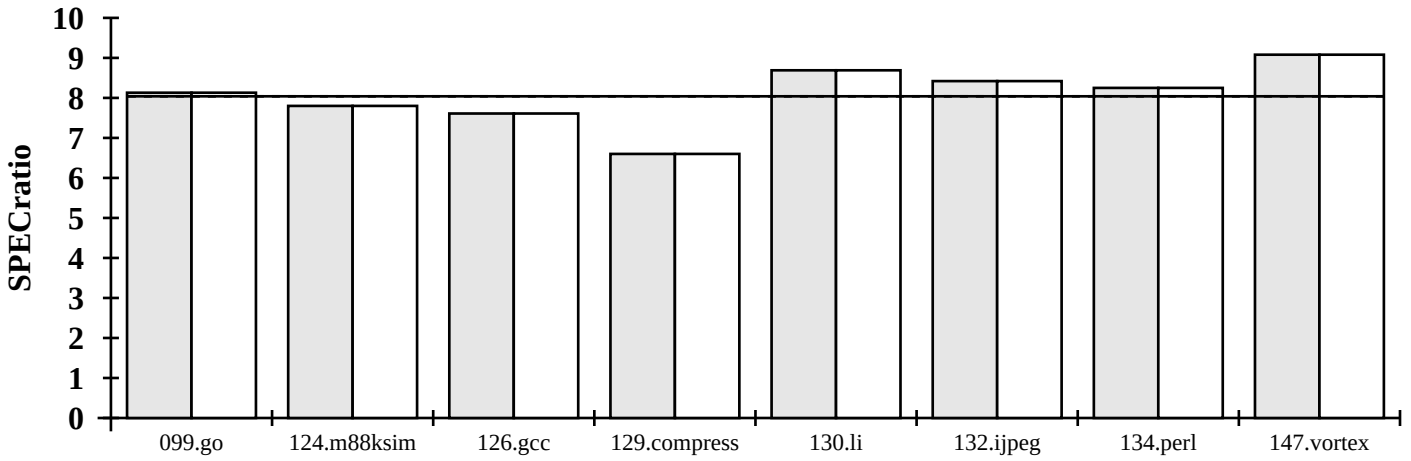
Intergraph Corporation
TDZ300 (200MHz)

SPECint95 = 8.04

SPECint_base95 = 8.04

SPEC license # 14	Tested By: Intel	Test Date: Oct-95	Hardware Avail: Mar-96	Software Avail: Feb-96



Hardware/Software Configuration for:
TDZ300 (200MHz)

Hardware

Model Name:	TDZ300
CPU:	200MHz Pentium Pro Processor
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD
Secondary Cache:	256KB(I+D)
Other Cache:	None
Memory:	128MB (60ns fast page)
Disk Subsystem:	2GB ST32550W
Other Hardware:	AHA-2940W Controller

Software

Operating System:	UnixWare 2.0, SDK
Compiler:	Intel C Reference Compiler 2.2 Beta
File System:	ufs, vxfs (/tmp as 8MB /tmpfs)
System State:	Single user (root + killall)

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
099.go	4600	566	8.13	566	8.13
124.m88ksim	1900	244	7.80	244	7.80
126.gcc	1700	223	7.61	223	7.61
129.compress	1800	273	6.60	273	6.60
130.li	1900	219	8.69	219	8.69
132.ijpeg	2400	285	8.42	285	8.42
134.perl	1900	230	8.25	230	8.25
147.vortex	2700	297	9.08	297	9.08
SPECint_base95 (G. Mean)			8.04		
		SPECint95 (G. Mean)			8.04

Notes/Tuning Information

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Base and non-base flags are the same and use Feedback Directed Optimization
Pass1: -tp p6 -ipo -xi -prof_gen -ircdb_dir /tmp/IRCDB
Pass2: -tp p6 -ipo -xi -prof_use -ircdb_dir /tmp/IRCDB
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
Memory subsystem is four-way interleaved.  Fastest memory timings selected in bios.
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