SPEC CINT95rate Results

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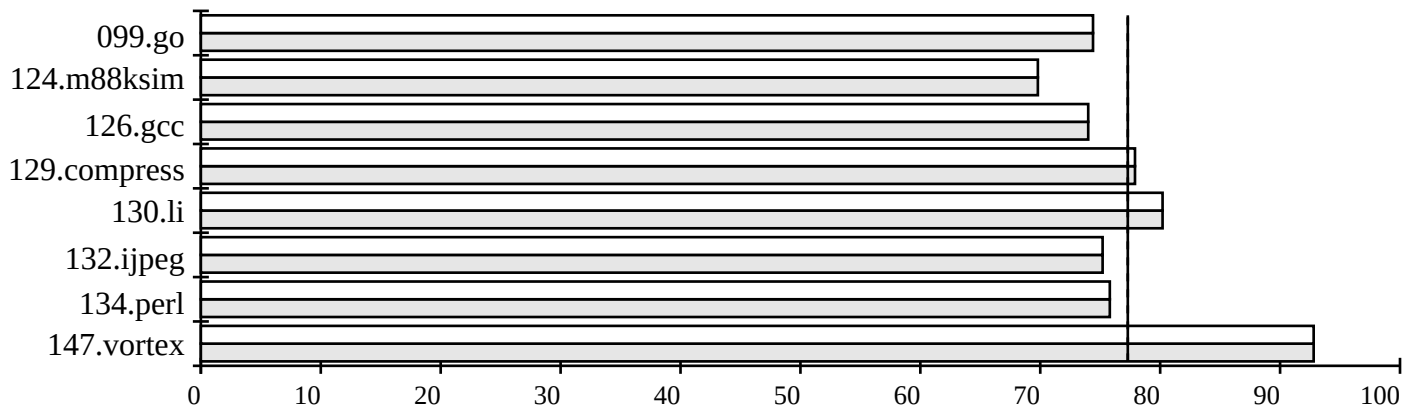
UNISYS Corporation
ClearPath SMP6400 1-CPU(200MHz)

SPECint_rate95 = 77.3

SPECint_rate_base95 = 77.3

SPEC license # 15	Tested By: UNISYS	Test Date: Jan-97	Hardware Avail: Jan-97	Software Avail: Jan-97



Hardware/Software Configuration for:
ClearPath SMP6400 1-CPU(200MHz)

Hardware

Model Name:	ClearPath SMP6400 1-CPU(200MHz)
CPU:	Pentium Pro Processor 200MHz
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI + 8KBD
Secondary Cache:	512KB(I+D)
Other Cache:	None
Memory:	256MB
Disk Subsystem:	4GB SCSI-2
Other Hardware:	None

Software

Operating System:	SCO UnixWare 2.1
Compiler:	Intel C Reference Compiler V2.3.2
File System:	vxfs
System State:	Multi User

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	4	2225	74.4	4	2225	74.4
124.m88ksim	4	980	69.8	4	980	69.8
126.gcc	4	828	74.0	4	828	74.0
129.compress	4	832	77.9	4	832	77.9
130.li	4	852	80.2	4	852	80.2
132.ijpeg	4	1149	75.2	4	1149	75.2
134.perl	4	902	75.8	4	902	75.8
147.vortex	4	1048	92.8	4	1048	92.8
SPECint_rate_base95 (G. Mean)			77.3			
			SPECint_rate95 (G. Mean)			77.3

Notes/Tuning Information

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Base and peak flags are same and use Feedback Directed Optimization
Pass1: -tp p6 -ipo -xi -prof_gen -ircdb_dir /pan/IRCDB
Pass2: -tp p6 -ipo -xi -prof_use -ircdb_dir /pan/IRCDB
-ircdb_dir is a location flag and not an optimization flag
Portibility : 124: -DSYSV -DLEHOST 130, 134, 147: -lm 132: -DSYSV 126: -lm -lc -L/usr/ucblib -lucb -lmalloc
Four processor module: Other processors disabled with UnixWare "psradm" command
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For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@specbench.org
http://www.specbench.org/

-- Prepared By: --