SPEC SERT 2 Suite Platform Acceptance Certificate



ISO / IEC 21836:2020 Ver. E

Client Configuration Reference Intel_Lin_OJDK17

Platform Intel® Xeon® 5th Gen -- RHEL 9 -- OJDK 17 (17+35-2724)

Platform Owner Sanjay Sharma -- Intel Corporation

Acceptance Scope CPU Architecture

CPU Architecture Intel® Xeon® 5th Gen CPU Architecture Class Intel® Xeon® CPU Model(s) X5XX+

OS RHEL 9

JVM OJDK 17 (17+35-2724)

Memory Memory Size: 1024GB DIMMs per Channel: 1, DIMMs per Socket: 8 ES -- Minimum supported Memory size: 16 GB ES -- Maximum supported Memory Size: 4096 GB

Storage HDD Quantity: 1, NVMe Quantity: , SSD Quantity: 1

SERTv2 2.0.7

Reference(s) 0

NOTE: Some details may be obfuscated for pre-release hardware, data behind this Platform Acceptance is available from SPEC upon request -- e-mail sertsupport@spec.org

ISO / IEC 21836 Requirement	System: Configuration Summary	Pass / Fail
9.3.5.4.2.2, Validity & Variance	ES: X5XX+ 1024GB @ 5.6GT/s 1, 1	Pass
9.3.5.4.2.3, CPU socket test	ES: X5XX+ 1024GB @ 5.6GT/s 1, 1	Pass
9.3.5.4.2.5, CPU core count test	ES: X5XX+ 1024GB @ 5.6GT/s 1, 1	Pass
9.3.5.4.2.6, Memory size test	ES: X5XX2 1024GB @ 5.6GT/s 1, 1	Pass

2023-08-23

Copyright © 2006-2022 Standard Performance Evaluation Corporation (SPEC). All rights reserved, see http://www.spec.org SPEC, the SPEC logo, and the names PTDaemon, SPECpower are registered trademarks of SPEC and SERT is a trademark of SPEC. Additional product and service names mentioned herein may be the trademarks of their respective owners.